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To: Examiner Andy Huynh	Total Pages Sent: 10
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Facsimile Number: 571-273-8300	Transmission Date: October 20, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Schloesser, et al. Docket No.: INF-135
Serial No: 10/777,128 Art Unit: 2818
Date Filed: February 13, 2004
Title: Method of Fabricating and Architecture for Vertical Transistor Cells and Transistor-Controlled Memory Cells

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- Certification of Facsimile Transmission (1 page)
- Amendment (9 pages)

Respectfully submitted,


Anne Marie James
Legal Assistant

Confirmation Respectfully Requested

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Mail Stop: Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Dear Sir:

The following amendments and remarks are presented in response to the Examiner's Office Action mailed July 20, 2005. Please amend the above-referenced application as follows.